

10/584705  
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : Unassigned  
Applicant : Aviral Mittal  
Filed : Concurrently  
TC/A.U. : Unassigned  
Examiner : Unassigned

Confirmation No. Unassigned  
JUN 23 2006

Docket No. : **GB03 0231US1**  
Customer No. : 24738

Title: : Delay Fault Test Circuitry and Related Method  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**PRELIMINARY AMENDMENT**

Sir:

Prior to calculation of the filing fee and examination please amend the above-identified application as follows:

**Amendments to the Specification** there are no amendments in this paper.

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks/Arguments** begin on page 6 of this paper.